

Notice of References Cited	Application/Control No. 10/751,170	Applicant(s)/Patent Under Reexamination DHANOA, KULWINDER	
	Examiner Paul W. Schlie	Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,630,096	05-1997	Zuravleff et al.	711/154
*	B	US-5,822,772	10-1998	Chan et al.	711/158
*	C	US-2003/0159008	08-2003	Sprangle et al.	711/154
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	ARM, "ARM PrimeCell(tm) MultiPort Memory Controller (PL176) revision r0p1, Technical Reference Manual" June 2003, ARM, pp Chapter 1-2
	V	ARM, "ARM PrimeCell(tm) SDRAM Controller (PL170) Technical Reference Manual", June 2001, ARM
	W	Aurora VLSI Inc. "AMBA Bus Master Specification" March 2002, Aurora VLSI Inc.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.